

Micro Device Modeling Method and Design for Manufacturability

Zheng Liu*, Bo Sun

School of Mechatronic Engineering, Xi'an Technological University, Xi'an China

*Corresponding author, e-mail: zheng.liumail@gmail.com

Abstract

The micro device designing flow begins with the mask design currently, which is not intuitive for designer and sometime made them confused. As a result, the manufacturability is hard to prove in advance with the traditional designing way. Especially for surface micro-machined device with multiple structural layers, the 2D mask design cannot ensure the fabrication of complex 3D device with high yields reliably. The rise of structural designing strategies gave a direction to change the traditional habit. On the other hand, the top-down method provides the advanced designing flow, which begins with the system level design. However, there still some key technologies to study further because of the novel design procedure. To improve the manufacturability of micro device, the designing flow based on design for manufacturability methodology is presented, which draw on the advantages and experience of the mechanical design method. The key technologies on the structural design level, the processes planning level and the fabricating level are introduced correspondingly. In addition, we present the framework to implement the method.

Keywords: design for manufacturability, micro device, optimal design, computer aided design

Copyright © 2013 Universitas Ahmad Dahlan. All rights reserved.

1. Introduction

Along with the increasing demands, the most challenge faced in micro device area is the marketization. The traditional designing flow is derived from integrated circuit fabricating processes, which is bottom-up flow and summarized as mask-to-shape-to-verify [1]. Taking the more complex structure into account, it makes the micro device designing process poorly intuitive and unreliable. Especially for surface micromachining, it is too much for the designer to confirm all the processes of multiple layers in advance. Furthermore, the more complex structure and constraint will make this situation worse. Therefore, it is the advanced designing flow to begin with the 3D model from which the fabricating information is derived [2, 3]. Because this designing flow is inverse in contrast with the traditional one, however, how to build the relationship between designing model and the fabricating process becomes an important step to improve manufacturability. It is also the key point of the top-down design methodology [4].

The mechanical designing tools are more mature, which, although belonging to different region, afford lessons meriting attention [5]. Because the fabricating characteristic, the tools evolved from integrated circuit design are still popular [6, 7]. To overcome the shortcoming of those tools, the top-down design method is proposed, which currently faces many challenges [8]. Implementing DFM (design for manufacturability) is conducive to improve manufacturability, which is also the key challenge to realize the top-down method [9]. However, how to comprehensively introduce the design for manufacturability method into the micro device design with the standardized processes is still require study further [10, 11]. With respect to model construction, the feature technology in micro device modeling improves designing efficiency [12]. With the method, in some instances, even the mechanical tools can be used to improve designing efficiency [13, 14]. Nevertheless, because the characteristic of the micro fabricating, there is still no method effective enough to combine the feature technology with the processes generation. Current works focus on system level modeling and simulation [15, 16]. Where the overall designing flow of micro device is concerned, there are much more research of solving problem of "function-to-shape" than research in the stage of "shape-to-mask" [17]. In fact, the latter is the key point to improve manufacturability. To introduce the design for manufacturability

idea into the designing flow of micro device, the framework and according key technologies are presented in this paper.

2. Modeling Methods Evolution and Tools Development

The deficiency of the traditional designing flow is illustrated in Figure 1.

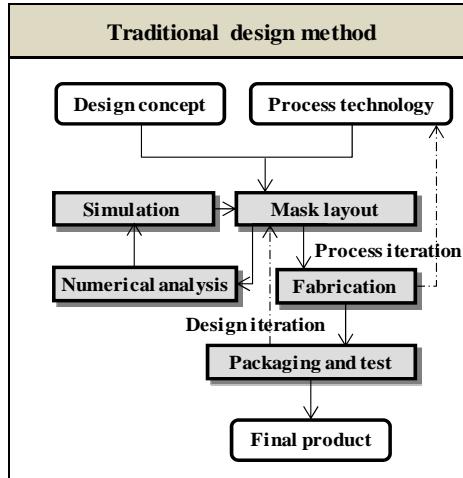


Figure 1. The Traditional Designing Flow

The traditional design method begins with mask layout design. On one hand the process is concerned in the early stage, on the other the consequent two iterations make designing work time-consuming and inaccurate. In contrast, illustrated in Figure 2, the structural design method is more convenient for designer.

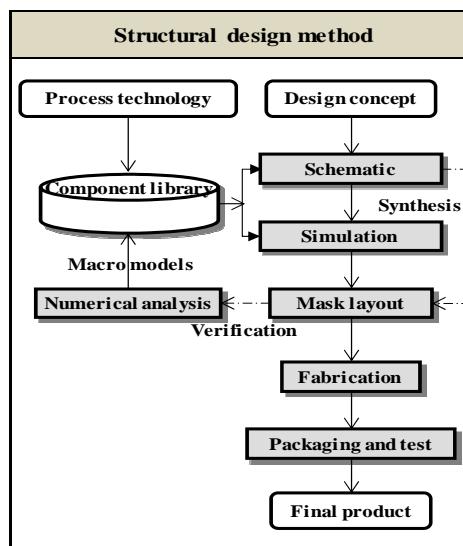


Figure 2. The Structural Design Method

For structural design method, the component library and schematic synthesis improves the designing efficiency. However, the problem to improve manufacturability emerged because of the new flow of simulation-to-mask-to-fabrication. How to improve the manufacturability

according with the advance high level modeling method still needs further study. To ensure better manufacturability, the DFM-oriented top-down design method is presented as shown in Figure 3.

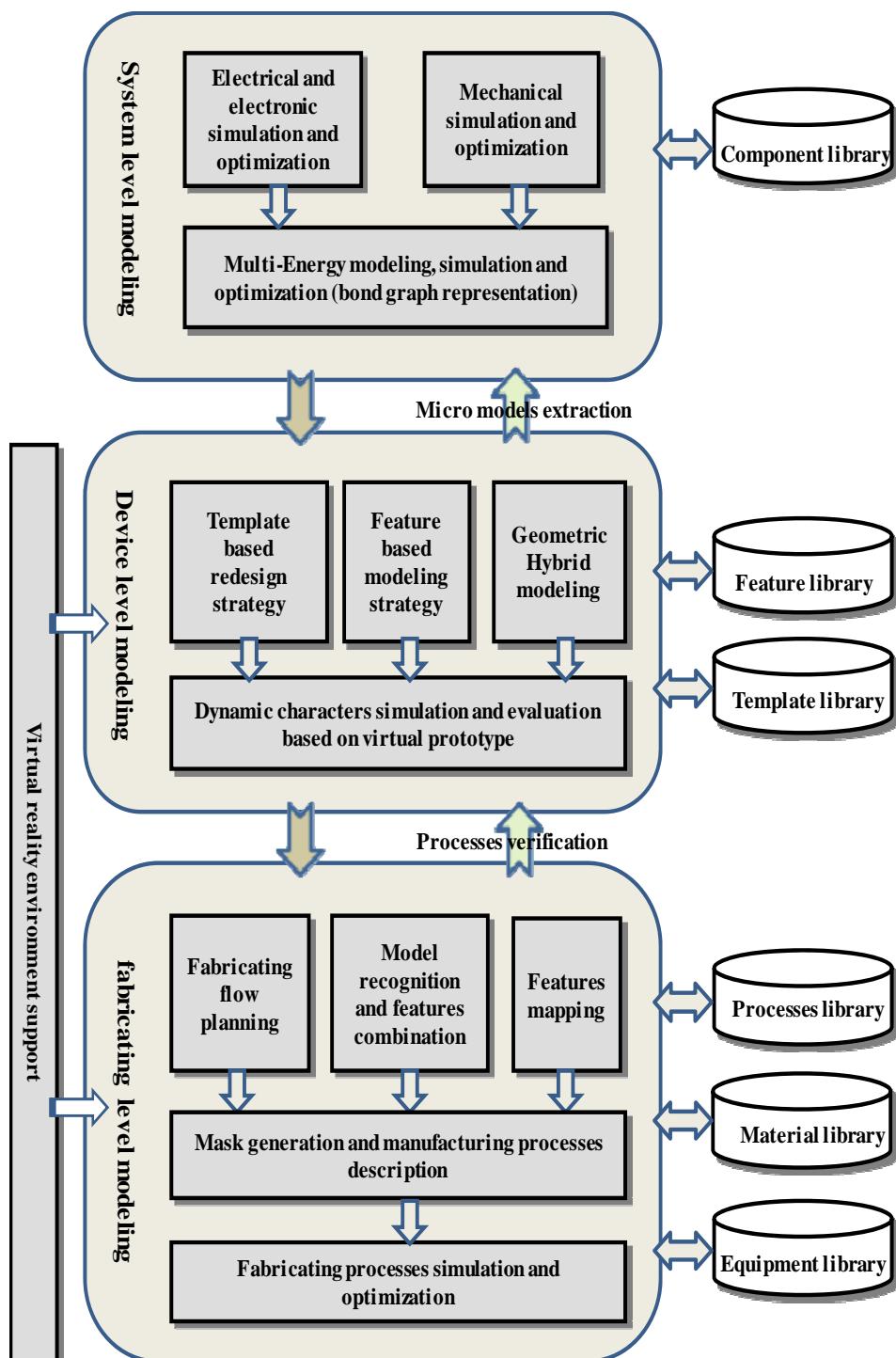


Figure 3. The Three Levels of Top-down Design Method

The first level is system level designing, which deals with simulation and optimization of both electrical and mechanical components. As the effective way of analyzing the traditional

mechanical structure, the multi-physics simulating method is introduced into the area of micro device [18]. To perform the task of modeling and simulation of multi-energy domain, bond graph is applied on the support of components library. In the device level modeling, the 3D model of micro device is constructed. Three ways are supplied to build the model. If the structure of the new designing instance is similar to the template, that is, the codes of the new structure matches the codes retrieved from the template library on the basis of Neural Networks technology, the parameters of the template are revised to construct the new model. For example, a series of micro spring can be constructed with facility by means of the parametric template. The actual structure of the spring agrees with the desired one and conforms to LIGA technology [19]. While for those not matching any template, the parametric features are provided to build the model. These features are revisable 3D elements satisfying the intuitive modeling habit, which are stored in the flexible and extensible feature library. Taking the complexity of device into account, the hybrid modeling method is also permitted to deal with arbitrary structures. After model construction, the dynamic characters simulation and evaluation is performed based on virtual prototype method. Finally, the fabricating level modeling happens, which is completed in a features mapping way. For surface micro-machining, the procedures of features combination and fabricating flow planning are followed to carry out the layering operation. The information of mask is derived from the 3D model by means of geometric algorithm. With the derived process model, the corresponding simulation and optimization is finished before fabrication. To improve the intuition of modeling processes, the virtual reality environment is constructed to support the latter modeling stages. At the same time, the two feedbacks between different levels provide better manufacturability.

3. Design for Manufacturability in Micro Device Designing

To introduce design for manufacturability methodology into the designing process of micro device, the stages involved are taken into account to provide the overall constraint. Besides the conceptual designing stage, there are three stages in designing cycle, in which the DFM elements work to cooperate with the top-down designing flow. The main factors to apply the design for manufacturability strategy are as follows:

1) Structural design stage

Above all, the models are constructed with feature technology. The functional and structural models consist of functional components and structural features correspondingly. These features themselves possess good manufacturability in a sense. When combined together, however, more constraint should be taken into account. The designing rules are based on MUMPs standard, which is a commercial program that provides cost-effective, proof-of-concept MEMS fabrication. For the devices that are common to use, the templates are built up to make the models of these devices parametric. When new designing task comes, it is compared with the templates firstly. The similar instance is retrieved from the library, which is parameterized to accommodate to the new task. As for the templates, each of them has good manufacturability by means of the constraint on the adjusting limit of the parameters.

2) Processes design stage

In the processes design stage, the mapping relationship between designing model and process model is constructed. The designing model is based on hybrid modeling representation technology. However, the overall consideration of fabrication is insufficient with the original designing model. When mapped into process model, which is organized with process features, the constraint features based on standardized processes take essential effect to improve manufacturability.

3) Test and fabricating stage

Before fabricating, the geometric and physical simulation is carried out to verify the manufacturability of the previous models. The consequent feedback directs the revision process. Together with the feedback occurring in processes designing stage, they constitute the feedback flow of the designing circle.

The design for manufacturability strategy in different stages is illustrated in Figure 4.

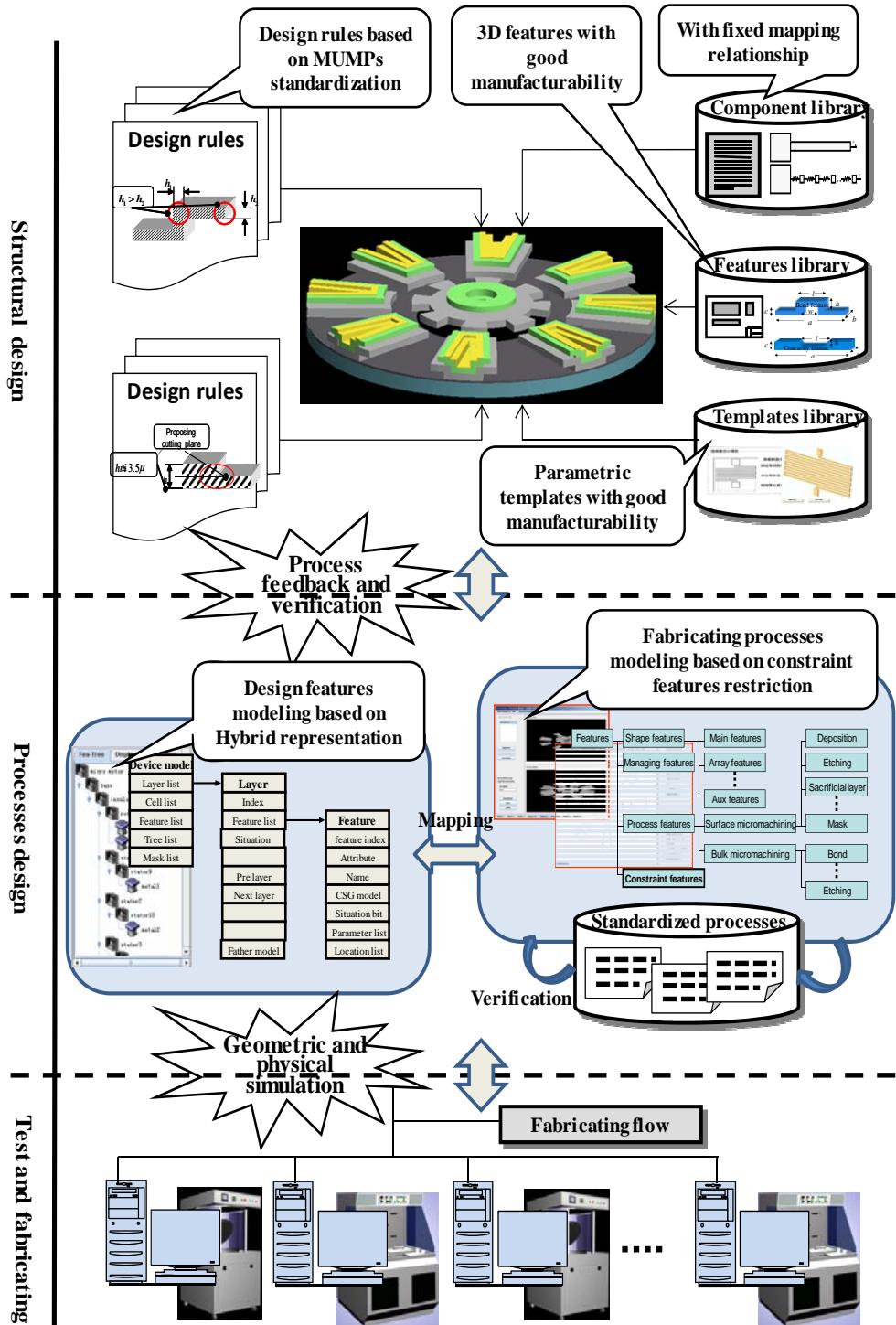


Figure 4. The Design for Manufacturability Strategies

4. The System Framework

To implement the DFM-based designing method, the framework is illustrated in Figure 5. The modules are arranged in the cooperative designing environment with distributed database.

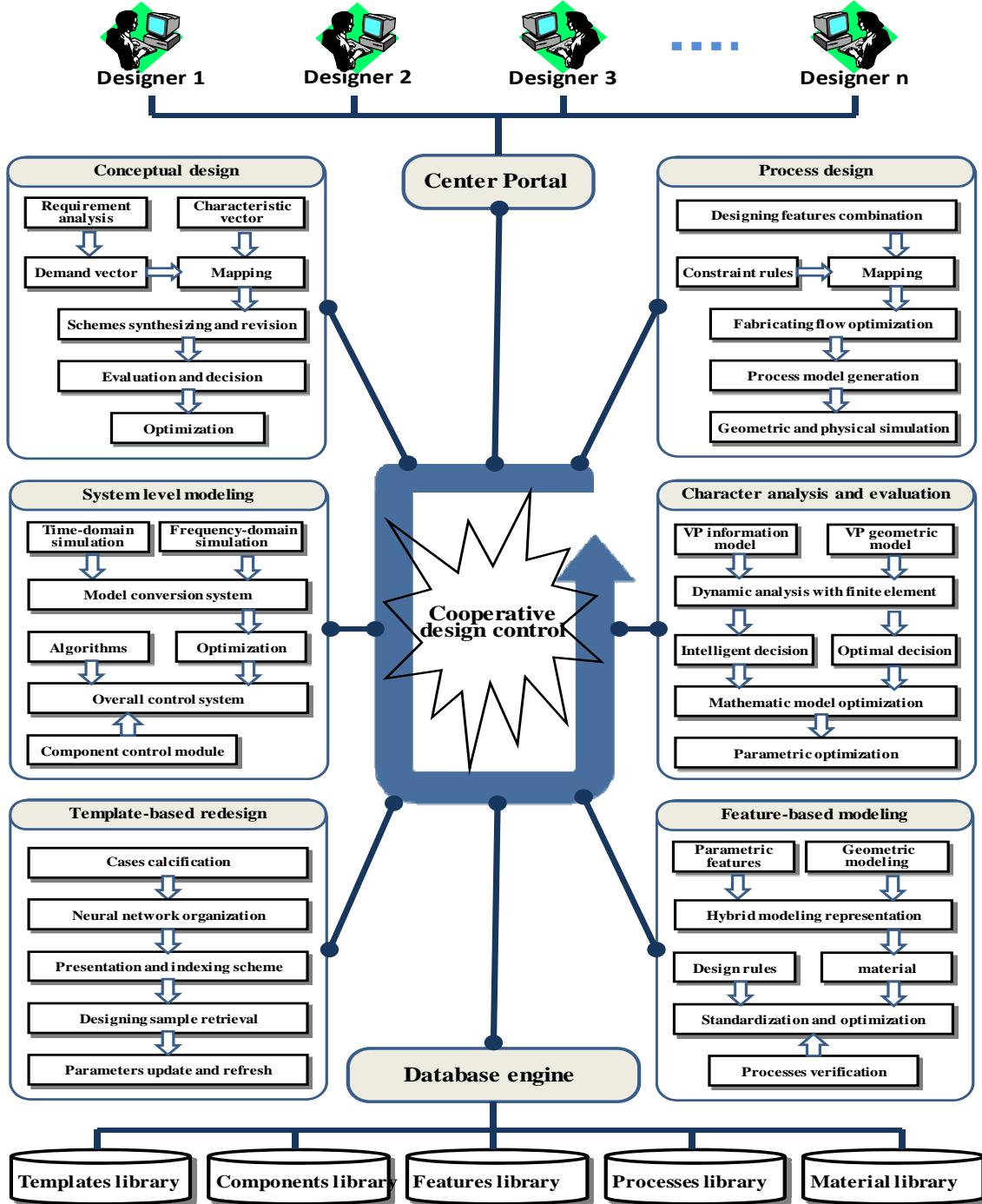


Figure 5. The Framework and Modules of the System

1) System level design

The functional model is constructed with bond graph method. After the simulation in time domain and frequency domain, the model is optimized by control system. Based on mapping algorithm, the functional model corresponds to structural model.

2) Template-based redesign

The previous instances are parameterized and coded storing in template library. After classifying the cases, the template system is organized by neural networks. When new designing work coming, it is retrieved from the library to find the matching template. After

parameters revising, the model is update. The system does not need to match the instance absolutely. The mismatched parts can be built with other ways.

3) Feature-based modeling

The parametric features are used as the fundamental components to build up the 3D structural model. For those parts that are too irregular to match features in library, the geometric modeling way is provided to construct the appearance, which relies on the geometric elements and modeling commands. The structural model is the feature-based hybrid representing model.

4) Processes design

The designing features combination procedure deals with the construction of layer information for surface micromachining, in other words, how the features are combined together as one layer. The process features is fabricating-oriented, such as etching feature, depositing feature and sacrificial layer feature. The mapping relationship is built between the different kinds of features. Meanwhile, the unreasonable structures are revised in accordance with the constraint features to improve the manufacturability.

5. Conclusion

The design for manufacturability method for micro device is explored and proposed. Above all, this paper gives an overview of the modeling methods that are considered to be the critical components of designing theory of micro device. With the indication of how designing method evolves, the key technologies to implement the micro device design for manufacturability are presented. This enables designers to model the device more efficiently and intuitively, especially for complex surface micro-machined device that has multiple layers. As a result, the geometric model fulfilling the fabricating requirements is efficiently generated and mapped to fabricating model. Besides improving manufacturability of designing model, the method helps reduce produce development circle beyond the traditional design flow.

Acknowledgments

This work was financially supported by Natural Science Basic Research Plan in Shaanxi Province of China (Program No. 2013JM7029), the Science and Technology Development Plan Foundation of Shaanxi Province (No. 2011K07-11), Scientific Research Program Funded by Shaanxi Provincial Education Department (Program No. 11JK0864), President Fund of Xi'an Technological University (No. XAGDXJJ1007) and Shaanxi Major Subject Construction Project.

References

- [1] Schlipf M, Bathurst S, Kippenbrock K, Kim SG, Lanza G. A structured approach to integrate MEMS and Precision Engineering methods. *CIRP Journal of Manufacturing Science and Technology*. 2010; 3(3): 236-247.
- [2] Li J, Gao S, Liu Y. Feature-based process layer modeling for surface micro-machined MEMS. *Journal of Micromechanics and Microengineering*. 2005; 15(3): 620-635.
- [3] Xu J, Yuan W, Xie J, Chang H. A MEMS CAD methodology from 3D model to 2D mask layout. *China Mechanical Engineering*. 2008; 19(1): 80-84.
- [4] Fedder GK. *Top-Down Design of MEMS*. Proceedings of the 2000 Int. Conf. on Modeling and Simulation of Microsystems Semiconductors, Sensors and Actuators. San Diego(USA). 2000; 1: 7-10.
- [5] Zhang C, Lu D, Jiang Z. Study on a MEMS CAD System Based on SolidWorks. *Applied Mechanics and Materials*. 2008; 10(1): 772-776.
- [6] Zhang H, Guo H, Zhang D, Xu J, He Y. Research on Computer Aided MEMS Process Integration Technology. *Nanotechnology and Precision Engineering*. 2004; 2(3): 229-233.
- [7] Chang H, Xie J, Xu J, Yan Z, Yuan W. One Novel MEMS Integrated Design Tool with Maximal Six Design Flows. *Chinese Journal of Sensors and Actuators*. 2006; 19(5): 1323-1326.
- [8] McCorquodale MS, Gebara FH, Kraver KL, Marsman ED, Senger RM, Brown RB. *A Top-Down Microsystems Design Methodology and Associated Challenges*. Proceedings of the conference on Design, Automation and Test in Europe. Munich(Germany). 2003; 1: 292-296.
- [9] DaSilva MG. *Design for Manufacturability for 3D Micro Devices*. NSF Workshop on Three-Dimensional Nanomanufacturing: Partnering with Industry. Birmingham(USA). 2010; 1: 5-9.
- [10] Khan F, Bazaz S, Sohail M. Design, Implementation and Testing of Electrostatic SOI Mumps Based Microgripper. *Microsystem Technologies*. 2010; 16(11): 1957-1965.

- [11] Hu F, Yao J, Qiu C, Ren H. A MEMS Micromirror Driven by Electrostatic Force. *Journal of Electrostatics*. 2010; 68(3): 237-242.
- [12] Gao F, Hong YS. *Function-Oriented Geometric Design Approach to Surface Micromachined MEMS*. Technical Proceedings of the 2004 NSTI Nanotechnology Conference and Trade Show. Boston (USA). 2004; 1: 319-322.
- [13] Li J, Gao S, Liu Y. Solid-Based CAPP for Surface Micromachined MEMS Design. *Computer-Aided Design*. 2007; 39(3): 190-201.
- [14] Zhang C, Jiang Z, Lu D, Ren T, Wang J. Design for Micro-Electro-Mechanical Systems Devices Based on Three-Dimensional Features. *Journal of Xi'an Jiaotong University*. 2007; 41(5): 571-575.
- [15] Xu J, Yuan W, Chang H, Yu Y, Ma B. Angularly Parameterized Macromodel Extraction for MEMS Structures with Large Number of Terminals. *Journal of System Simulation*. 2010; 22(3): 748-751.
- [16] Liu Y, Jiang P, Zhang D. 3D-Feature-Based Structure Design for Silicon Fabrication of Micro Devices. *Microsystem Technologies*. 2007; 13(7): 701-714.
- [17] Fan Z, Wang J, Achiche S, Goodman E, Rosenberg R. Structured Synthesis of MEMS Using Evolutionary Approaches. *Applied Soft Computing*. 2008; 8(1): 579-589.
- [18] Zhang Y, Huang X, Huang T, Ruan J, Wu X. Ventilation Structure Improvement of Air-cooled Induction Motor Using Multiphysics Simulations. *TELKOMNIKA Indonesian Journal of Electrical Engineering*. 2012; 10(3): 451-458.
- [19] Li G, Sui L, Shi G. Study on the Linearly Range of S-Shaped MEMS Planar Micro-spring. *TELKOMNIKA Indonesian Journal of Electrical Engineering*. 2012; 10(6): 1327-1332.