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# Distributed MPP Tracking of PV through Buck Converter Using Fuzzy

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### Abstract

Photovoltaics (PV) are the most abundant, perennial, environment friendly and distributed source of energy all over the globe. In this paper, the method to increase PV extraction efficiency for 60W panel with improved DMPP (Distributed Maximum Power Point) even on changing temperature and irradiance have been mapped and discussed. In order to achieve this, the components and subsystems have been analyzed and validated. The validated models are used to maximize the power output using two different models of DC-DC Converters in MATLAB/SIMULINK environment. Closed loop Buck converter using state space nonlinear differential equations and direct component model are compared to reveal best results at Standard Test Conditions (STC). MPPT model developed can be used for obtaining maximum power output from PV even in partial presence of sun using Fuzzy Logic Controller. The Controller designed tracks highest power output for the buck converter. The model developed, using tested membership functions in FLC, can serve a platform for various real time applications using PV.

Keywords: Photovoltaics, MATLAB/SIMULINK, Buck converter, STC

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# 1. Introduction

Solar energy maintains all life on earth. Solar is an emerging sustainable technology with immense potential to contribute largest investment in Green technology applications. Due to shortage and environmental impact of conventional fuels, solar powered systems assume importance to overcome hurdles and promote economic advantages. Easy implementation and design demands in electricity sector are making PV use significant in electricity generation and distribution purposes. Globally 1.5 billion people with no access to electricity can be developed through widespread adoption in PV innovative business [1].

PV modules are constructed using solar cells. Solar cells work on photoelectric effect converting sunlight into electricity. Since the output available from single solar cell is very small, series or parallel combination is preferred to obtain higher output from panel. Even under changing environmental conditions, efficiency of panel needs to be streamlined. For this, solar panel is under distributed conditions need to be operated at MPP (maximum power point). MPP describes a fixed operating point on IV (Current-Voltage) and PV (Power-Voltage) characteristic curves of solar cell. STC (standard test conditions) are desired with temperature of 25 °C (298.15K) and an irradiance of 1000 W/m<sup>2</sup> to maintain MPP in all cases [2].



Figure 1. PV panel structures; (a) Crystalline, (b) Thin Film

To obtain MPP in distributed conditions of changing temperature and irradiance, MPPT is designed. PV panels available in market include Crystalline (mono or poly) and Thin Film structures. The advantages of using fixed crystalline panels are that they use chemical silicon while thin film structures use amorphous silicon based on vapour deposition. Figure 1 shows both panel structures.

# 2. Research Method

In this research paper, firstly a systematic analysis of solar panel module based on mathematical modeling in Simulink-MATLAB is performed [3]. Thereafter, its investigation is performed for variable temperature and irradiance. MPP is successfully obtained [4]. The MPP tracking system for changing conditions is implemented using DC/DC Buck Converter followed by an FLC to monitor output of Converter. An estimation of two different models of buck converter is done using different temperature and irradiance conditions.

PV panel modeling for solar cells was done in SIMULINK-MATLAB using solar panel equations expressed as:

Thermal Voltage Equation

$V_{\rm T} = k_{\rm B} T_{\rm OPT} / q$	(1
Diode Current Equation	

$I_{\rm D}$ = N <sub>p</sub> I <sub>S</sub> [e <sup>(V/Ns) + (IRs/Ns)/N V</sup> <sub>T</sub> <sup>C</sup> -1]	(2)

Load Current Equation	
$I_{L} = I_{Ph} N_{p} I_{D} I_{SH}$	(3)
Photocurrent Equation	

i notocurrent Equa		
$I_{ph} = [k_i (T_{OPT} - T_{REF}) \cdot$	+I <sub>SC</sub> ] I <sub>RR</sub>	(4)

Shunt Current Equation  $I_{SH} = (IR_S+V)/R_{SH}$ 

Reverse Saturation Current  $I_{s} = [I_{Rs} (T_{OPT}/T_{REF})^{3} * q^{2}Eg/N k_{B} * e^{(1/T}_{OPT} - 1/T}_{REF})$ (6)

Reverse Current Equation I<sub>RS</sub>= I<sub>SC</sub>/ [e<sup> (q V</sup><sub>OC</sub><sup>/k CT</sup><sub>i</sub>OPT<sup>)</sup>-1]

Output Power P=VI

(8)

(7)

(5)

Where,

Table 1.	Terminology for constructing solar panel
VT	Thermal Voltage
V	Operating Voltage
Voc	Open ckt voltage
I <sub>SC</sub>	Short circuit current
Is	Reverse Saturation Current of Diode
I <sub>ph</sub>	Photocurrent
Ĺ	Cell Output Current
T <sub>REF</sub>	Reference Temperature of cell
T <sub>OPT</sub>	Operating Temperature
R <sub>SH</sub>	Shunt Resistance of Cell
Rs	Series Resistance of Cell
Eg	Energy Band Gap
N	Ideality Factor
kB	Boltzmann constant
ki	Current Proportionality constant
q	Electron charge
G	Irradiance
Ns	No. of cells in series
С	No. of Cells in module

Solar panel modeled is tested for STC with specifications given below,

Table 2. Spe	cifications of model				
Characteristics at STC with					
G=1KW/m <sup>2</sup> a	ind T=25°C				
P <sub>MPP</sub>	59.39 W				
V <sub>MP</sub>	16.64 V				
I <sub>MP</sub>	3.567 A				
Isc	3.7981 A				
V <sub>OC</sub>	21.07 V				

Figure 2 shows complete subsystem designed.



Figure 2. Solar Panel Subsystem

When model is simulated in MATLAB, Simout values available at PV subsystem output appear as shown in Table 3 for variable temperature and Table 4 for variable irradiance.

i u		Onnout	variabiec		inging it	mperate	
	Т°С	V <sub>oc</sub>	I <sub>SC</sub>	V <sub>MPP</sub>	I <sub>MPP</sub>	P <sub>MPP</sub>	
_	5	21.31	3.754	18.06	3.317	59.92	
	10	21.25	3.765	16.69	3.578	59.75	
	15	21.19	3.776	16.68	3.575	59.65	
	20	21.13	3.787	16.66	3.571	59.53	
	25	21.07	3.798	16.64	3.567	59.39	
	30	21.01	3.809	16.62	3.563	59.23	
	35	20.95	3.820	16.60	3.557	59.06	
	40	20.89	3.831	16.57	3.552	58.87	
	45	20.83	3.842	16.54	3.545	58.67	

Table 3. Simout variables for changing Temperature

Table 4. Simout variable for changing Irradiance

Irradiance	V <sub>oc</sub>	I <sub>sc</sub>	V <sub>MPP</sub>	I <sub>MPP</sub>	P <sub>MPP</sub>
Step	20.41	2.278	16.40	2.109	34.61
Constant	21.07	3.798	16.64	3.567	59.39
Trapezoidal	20.78	3.038	17.12	2.752	47.12

The experiment highlights that reading of Open circuit voltage  $V_{OC}$  and Short circuit current  $I_{SC}$  are more sensitive to irradiance variations as compared to temperature. The Open circuit voltage  $V_{OC}$  decreases sharply with increase in Short circuit current  $I_{SC}$ .

The characteristic curves obtained from Simout are shown in Figure 3.



Figure 3. IV and PV Characteristic curves

The characteristics of panel vary in accordance with varying temperature and irradiance when its output is obtained across a resistive load. The change observed is given by Dynamic Impedance of source given by expression:

$$\frac{\mathrm{d}V}{\mathrm{d}I} = -\frac{V}{I} \tag{9}$$

Thus, to monitor MPP in distributed conditions, MPPT is modeled as in Figure 4, using Buck Converter.



Figure 4. Blocks used in MPP Tracker circuit

## 3. Design Of Buck Converter

The output current, voltage or power characteristics of PV panel vary across load with change in temperature and irradiance. However, reduced and mismatch power output can be compensated by use of Converters and Controllers [5, 6]. Buck converters are used to decrease voltage at output. Basically, components used include MOSFET, diode, and inductor followed by filter capacitor and load at output. A control circuit is used to determine for how much voltage is required across load by converter using controller set point. For this, MOSFET is switched ON and OFF by Controller pulses on Gate. Figure 5 shows Buck converter.



Figure 5. Basic circuit of Buck converter

The pulses obtained at Gate of MOSFET depict converter operating frequency. A variation in converter operating frequency changes duty cycle of converter. Duty cycle is defined operation for which circuit works in ON state to duration for which it is ON and OFF. Two

different models are studied based on state space model equations and use of direct components available in MATLAB-SIMULINK. These are described below.

# 3.1. Model-A

State space model is determined considering binary values of inductor and capacitor. Discrete or continuous GUI model is selected. MOSFET is categorized as ON and OFF based on operational input pulse at gate. With this when MOSFET is considered ON, inductor current  $i_L(t)$  and the capacitor voltage  $v_C(t)$  appear:

$$\begin{cases} \frac{di_L}{dt} = \frac{1}{L} (V_{in} - v_o) \\ \frac{dv_o}{dt} = \frac{1}{C} (i_L - \frac{v_o}{R}), \quad 0 < t < dT, \quad Q:ON \end{cases}$$
(10)

And when the switch is OFF are presented by:

$$\begin{cases} \frac{di_L}{dt} = \frac{1}{L}(-v_o) \\ \frac{dv_o}{dt} = \frac{1}{C}(i_L - \frac{v_o}{R}), \quad dT < t < T, \quad Q:OFF \end{cases}$$
(11)

Using above equations, model is constructed as in Figure 6.



Figure 6. Buck Converter using state space variables

# 3.2. Model-B

Instead of using ON and OFF variables, this model uses direct components to determine output using pulse generator at gate. Figure 7 and Figure 8 shows modeling of Buck Converter with and without control pulse generator.



Figure 7. Buck Converter using Pulse Generator at gate



Figure 8. Buck Converter using controlled Pulse Generator at gate

Based on models discussed above, comparison is framed prior to outputs obtained across Converter 1, 2 and 3 for variable temperature and irradiance.

# 3.3. Variable Temperature

The measurements for variable temperature are expressed using three models of Buck Converter in Table 5. V  $_{CONV}$  shows control function using Controller to obtain voltage stabilization. V  $_{CONV1}$  uses steady space model output followed by V  $_{CONV2}$  that uses normal pulse given from Pulse Generator. Here irradiance G is kept constant at 1000 W/m<sup>2</sup>.

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T °C	Voc	V <sub>CONV</sub>	V <sub>CONV 1</sub>	V <sub>CONV2</sub>
	desired	With	With state	With Pulse
		Controller	space	Generator
5	21.31	21.27	7.243	.01065
10	21.25	21.25	7.223	.01062
15	21.19	21.20	7.204	.01059
20	21.13	21.14	7.184	.01056
25	21.07	21.08	7.164	.01053
30	21.01	21.02	7.143	.01050
35	20.95	20.96	7.122	.01047
40	20.89	20.90	7.101	.01044
45	20.83	20.83	7.08	.01041

Table 5. Simout Readings using Buck converter for T °C

## 3.4. Variable Irradiance

To show variation of irradiance G, the models are analyzed for three different irradiance functions with constant temperature at 25°C. These are step, constant and trapezoidal types. Table 6 lists values of Model A and B for three different irradiance types.

Table 6. Panel and Converter output for variable temperature and Irradiance G

Т°С	V <sub>CONV</sub>	Converter ou	Itput		
		(V <sub>CONV 1</sub> and '	$(V_{CONV 1} and V_{CONV2})$		
		Constant G	Step G	Trapezoidal G	
15	21.20	0.01082	0.01078	0.01080	
20	21.14	0.01086	0.01082	0.01084	
25	21.08	0.01092	0.01088	0.01090	
30	21.02	0.01098	0.01094	0.01092	

# 3.5. Variable Temperature and Irradiance

Since changing temperature and irradiance parameters manually is time consuming, automatic tune block is introduced. On modeling variable block by Simulink for temperature and irradiance panel gives Table 7.

Table 7. Panel output for variable temperature and Irradiance G

1°C	Fixed Model	Variable model
15	21.19	21.57
20	21.13	21.77
25	21.07	21.86
30	21.01	21.95

Thus, to trace out fittest solution for MPP, voltage needs to be stabilized for results closer to STC. Thus Model B giving superior results over A is used for set point tracking. The implementation of controllers improves performance of Buck Converter. Design and implementation of FLC is described below.

#### 4. Fuzzy Logic Controller

A Controller is used to establish control functions for converter to monitor desired current, voltage or power at output of panel. Figure 9 shows basic block diagram using Controller, PV and Converter subsystem.



Figure 9. Block diagram of Controller

A fuzzy system is a knowledge-based system which utilizes fuzzy if-then rules and fuzzy logic in order to obtain the output of the system. The Fuzzy logic controller used adjusts the converter duty cycle. FLC can be easily tuned and efficiently used to monitor voltage output from panel even under time varying processes [7, 8]. The desired output is obtained by varying the design parameters as each membership function for temperature, voltage and duty cycle. The different processes in obtaining controlled outputs from FLC are given in Figure 10.



Figure 10. Processes used in FLC

A two-input single-output fuzzy logic controller is designed with the input variables as: the error (E) and change in error ( $\Delta$ E) scaled for voltage given by Equation (12) and (13).

$$E(n) = \frac{P(n) - P(n - 1)}{I(n) - I(n - 1)}$$
(12)

$$\Delta E(n) = E(n) - E(n-1)$$
 (13)

The output variable is duty cycle (D) of the converter given by Equation (14).

$$D = \frac{Vout}{Vin}$$
(14)

Various process used in designing of FLC are described as follows.

a) Process of Fuzzification: The input variables in a fuzzy control system are mapped into sets of membership functions termed "fuzzy sets". The process, of converting a crisp input value to a fuzzy value, is called "fuzzification". The input-output variables used, are given in Figure 11.



Figure 11. Input-Output variables of the FLC



Figure 12. Membership functions (a) Error Input (E) (b) change in error ( $\Delta E$ ) and (c) Output (D)

The input voltage values are scaled and normalized into values closer to 21.07V. Through the membership function, the related fuzzy values (0~1) can be estimated for each

fuzzy descriptor: NB, NS, Z, PS, and PB namely, NB negative big, NS negative small, Z zero, PS positive small and PB positive big. Gaussian membership functions with 0.5 crossovers are used as they appear smooth and non-zero at all points providing less overshoot and undershoot with faster Rise time. These are shown in Figure 12.

The "mappings" of input variables into membership functions and truth values help the controller to make decisions for what action is to be taken based on a set of "rules".

b) Fuzzy Rules: The distinguishing mark of Fuzzy Logic in "rule-based". The rule base implements the expert knowledge in a form of IF-THEN rule structure.

Table 8 shows the fuzzy logic rules formulated.

Table 8. Fuzzy Rules						
ΔE	NB	NS	Z	PS	PB	_
Е						
NB	Z	Ζ	NB	NB	NB	_
NS	Z	Z	NS	NS	NS	
Z	NS	Z	Z	Z	PS	
PS	PS	PS	PS	Z	Z	
PB	PB	PB	PB	Z	Z	

The  $5 \times 5$ -rule matrix may be redefined in 25 rules:

If E (n) is NB and  $\Delta$ E (n) NB, then D is Z. If E (n) is NB and  $\Delta$ E (n) NS, then D is Z. If E (n) is NB and  $\Delta$ E (n) Z, then D is NB. If E (n) is NB and  $\Delta$ E (n) PS, then D is NB. If E (n) is NB and  $\Delta$ E (n) PB, then D is NB. If E (n) is NS and  $\Delta$ E (n) NB, then D is Z. If E (n) is NS and  $\Delta$ E (n) NS, then D is Z. If E (n) is NS and  $\Delta$ E (n) Z, then D is NS. If E (n) is NS and  $\Delta$ E (n) PS, then D is NS. If E (n) is NS and  $\Delta$ E (n) PB, then D is NS. If E (n) is Z and  $\Delta$ E (n) NB, then D is NS. If E (n) is Z and  $\Delta$ E (n) NS, then D is Z. If E (n) is Z and  $\Delta$ E (n) Z, then D is Z. If E (n) is Z and  $\Delta$ E (n) PS, then D is Z. If E (n) is Z and  $\Delta$ E (n) PB, then D is PS. If E (n) is PS and  $\Delta$ E (n) NB, then D is PS. If E (n) is PS and  $\Delta$ E (n) NS, then D is PS. If E (n) is PS and  $\Delta$ E (n) Z, then D is PS.

If E (n) is PS and  $\Delta$ E (n) Z, then D is PS. If E (n) is PS and  $\Delta$ E (n) PS, then D is Z. If E (n) is PS and  $\Delta$ E (n) PB, then D is Z.

If E (n) is PB and  $\Delta$ E (n) NB, then D is PB. If E (n) is PB and  $\Delta$ E (n) NS, then D is PB. If E (n) is PB and  $\Delta$ E (n) Z, then D is PB. If E (n) is PB and  $\Delta$ E (n) PS, then D is Z. If E (n) is PB and  $\Delta$ E (n) PB, then D is Z.

Considering any specific rule as for example, (1) when E is NB and  $\Delta E$  is NB, it means that E is higher than the voltage around MPP with a small  $\Delta E$  in voltage; we directly assign the duty cycle D to be Z for the drive. By using such a medium D, it is enough to make the exceeded E decreasing a little back to a suitable value. (2) When E is Z and  $\Delta E$  is Z, it means that E and  $\Delta E$  are at the medium values in the full range, i.e. E is lower than the voltage on MPP. Then, we have to assign the duty cycle D to be Z for the medium drive.

c) Defuzzification method: Defuzzification is the aggregation of the D from all rules, i.e. the duty cycles from 25 rules must be computed and combined for a specified crisp value of output. Defuzzification method gives a quantitative summary, i.e. given the possibility distribution of the fuzzy output, defuzzification amounts to selecting a single representative value that captures the essential meaning of the given distribution. The Defuzzification method used for the present case is the centroid method as this is the most prevalent and physically appealing of all the defuzzification methods. It is given by the algebraic expression:

$$D = \frac{\sum_{j=1}^{n} \mu(Dj) - Dj}{\sum_{j=1}^{n} \mu(Dj)}$$
(15)

Where D is the defuzzified value, Union of the membership functions is found by the MAX aggregation method and  $\mu$  (Dj) is the degree of the membership function.

The entire process of Implication, Aggregation and Defuzzification of the system is shown in the Rule View window of Fuzzy Logic Toolbox as described by Figure 13 and 14.



Figure 13. Fuzzy Rule Viewer

Figure 14. Fuzzy Rule Wizard



Figure 15 Fuzzy Logic Controller correcting Converter output

The controller makes feasible decisions for what action is to be taken. The universe of discourse for error input is taken [-8, +8] and change in error is chosen to be [-10, +10] for the panel voltage output and the output variable duty cycle is chosen to be as [-8, 8]. The range of the input variables can be changed according to the changing demand for the varying input. The Simulink model using FLC is given in Figure 15.

# 5. Results

The system was tested for various subsets of error and change in error with changeable crossover points. The converter outputs using fuzzy are shown in Table 9.

Table 9. FLC controlled output	
Parameter	Set point STC= 21.07 V
	Panel output for variable T= 21.86 V
Converter Output using FLC	21.40 V
Duty Cycle	0.978

It can be seen from the results of Figure above that the output of the system is under control and closer to set point even when the disturbance is added to the system. The model developed in this section shows that it can be easily implemented in the industry.

### 6. Conclusion

The panel characteristics using buck converter are implemented for variable temperature and irradiance monitoring Fuzzy controllers. The duty cycle is also reduced. Buck converters can monitor MPP more closely to STC by using selected membership functions and parameter modeling of FLC. Moreover, less cost for computing and faster response together with satisfactory results for FLC are observed.

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